

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Hiroshi HASHIMOTO, et al.**

Serial No.: **Not Yet Assigned**

Filed: **February 27, 2002**

For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD OF
PRODUCING THE SAME**



INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

February 27, 2002

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

Copies of the reference are enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge

Deposit Account No. 01-2340.

Respectfully submitted,
ARMSTRONG, WESTERMAN & HATTORI, LLP

A handwritten signature in black ink, appearing to read "William G. Kratz, Jr.".

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Enclosures: PTO-1449; References (2)

INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 020244	Serial No. New Appln. 10/083533
	Applicant(s): Hiroshi HASHIMOTO, et al.	
	Filing Date: February 27, 2002	Group Art Unit:

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U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					
_____	AE					

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
_____ AF 4-302174	10/26/92	Japan	Abstract
_____ AG 9-298281	11/18/97	Japan	Abstract
_____ AH			
_____ AI			
_____ AJ			

OTHER DOCUMENTS

_____	AK			
_____	AL			
<table border="1" style="width: 100%;"> <tr> <td style="width: 50%;">Examiner</td> <td style="width: 50%;">Date Considered</td> </tr> </table>			Examiner	Date Considered
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